



PATENT ABSTRACTS OF JAPAN

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(54) DIGITAL BROADCAST RECEIVER

(57) Abstract:

PROBLEM TO BE SOLVED: To provide a digital broadcast receiver that keeps a synchronization time within a prescribed time so as to attain high-speed channel selection and searching by controlling a carrier frequency to be demodulated in a way that a difference between a frequency of an intermediate frequency signal and the received carrier frequency to be demodulated by a demodulator is a prescribed value.

SOLUTION: Difference frequency detectors (9, 13) in the digital broadcast receiver (R) measure a difference destination (ΔF_{1st}) between an intermediate frequency ($F(sif)$) and a carrier frequency ($F(Sca)$) to be demodulated. Reception control sections (13, 7) corrects the carrier frequency ($F(Sca)$) so that the measured difference frequency (Δ) is within a 1st difference frequency region where a rate of change in a synchronization time with respect to the difference frequency between the intermediate frequency and the demodulation carrier frequency is small. Thus, the synchronization time (T_s) can be kept within a prescribed time by demodulating the intermediate fre-

quency ($F(sif)$) with the corrected demodulation carrier frequency ($F(Sca)$).

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